



Partly-Isolated DC-DC Converter for DC Bus Battery-PV Solar Energy System

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ABSTRACT

A partly-isolated three-port DC-DC converter for simultaneous power management of multiple energy sources is investigated in this paper. The converter topology composes of a bidirectional buck-boost and a flyback isolated switched-capacitor through a bidirectional converter. This proposed converter is the capability of interfacing sources of different voltage-current characteristics with a DC-link and/or a load. The proposed converter is applied for simultaneous power management of energy sources as photovoltaic (PV) panel, battery, and DC-link and/or load. The operation of the converter is based on a combination of three switches with four states that are created as a binary variable. It is necessary to set input generation sources and output voltage through various control variables to control the converter in each state. The effectiveness of the proposed converter is verified via simulation based on PSIM software and through experiments. The obtained results show that the converter is not only capable of maximum power point tracking control (MPPT) for the PV panel when there is solar radiation, more also can control the charge or discharge of the battery by its characteristic.

1. INTRODUCTION

The use of solar energy is only limited by human ingenuity. There can apply it in fields such as space heating and cooling through solar architecture, drinking water distillation and disinfection, daylighting, solar hot water, solar cooking, and high-temperature thermal processes for industrial purposes. Most importantly, solar energy can convert into electrical energy. Today, solar photovoltaic, using power electronics systems, has changed the image for utilizing solar energy [1]. The cost of solar cells with technology is getting cheaper [2]. However, there are still issues concerning the efficiency of the photovoltaic panels and the process of converting energy to the grid and load. The output voltage of low-power renewable energy sources is usually low wherefore it must use a DC-DC converter to increase the voltage and a DC-AC converter to connect with the power grid. The series connection of the above two converters is the DC-DC-AC multiplier converter that connects between the renewable energy sources the load or AC grid. Therefore, a high boost ratio DC-DC converter has many effects on the overall efficiency of the system [3-6]. The DC-DC boost-flyback converter cannot apply in high boost ratio applications due to some limitations. For example, the duty cycle in a boost circuit increases to be

close to 1 for high DC voltage gain resulting in the limited parameters in the circuit as the power switch element, coil, capacitor. This happens because of encountering electromagnetic interference and reverse-recovery issues at extreme duty cycles. Moreover, the large cause of power loss of the main switch is the energy leakage of the primary and secondary winding in the pulse transformer of the boost-flyback converter. Besides, the number of decentralized energy sources is continuously growing. So that it negatively affects the quality of the grid voltage [7, 8].

Nowadays, large amounts of the distributed energy sources are connected to the existing grid resulting in the effect of power quality in the connected area. The deterioration in power quality may result in the loss of connecting the DC-AC converter with the grid. So that is because the power quality is outside the set range of the inverter resulting in disability to feed power to the grid. In addition, the grid's power quality is not only the cause of the unreliable PV power generation system but also loads (electricity customers) experiencing a higher than rated grid voltage. To overcome the above problems, a lot of researches have been proposed effective solutions to improve the characteristic of the boost converter. For

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example, a switch with capacitors has been introduced in [9-13], the coupling inductor has been presented in [14-19], and the increased voltage is introduced in [20, 21]. The authors in [22] have proposed a DC-DC converter bidirectional multi-level in the power system DC sources. In this study, the output level can be changed almost continuously without any magnetic components. One of the major benefits of this magnetic-less system is that very high-temperature operation is possible in comparison to conventional solutions.

In [23], the authors have proposed a new bidirectional non-isolated three-port DC-DC converter is developed from the bidirectional DC-DC converter base boost and buck. The direct conversion energy is better performance. In this architecture, the converter is connected between the PV source pack, the battery pack, and the DC-link. In this case, the switching power of the converter is large voltage stress with the scenario of boost condition. In [24], the authors have proposed a single-inductor dual output switching converter topology that can independently regulate two output voltages; besides, the proposed solution use only one inductor and make four power switches. In this architecture, it is beneficial due to the reduction of one inductor. The proposed solution in [25] is considered as a further improvement method, in which a three switch DC-DC converter with two outputs is presented. The DC-DC converters bidirectional of high efficiency using soft circuit or parallel switching solutions is proposed in [26-28]. A comparative study shows current advancements in non-isolation bi-directional topologies [29]. Diagrams using two bridge converters with a large number of power switches are suggested, those schemes are suitable for high power and not for small power applications [30]. The use of the nonlinear voltage regulation algorithm for DC-DC boost converter is proposed in [31]. The modulation scheme for switching DC-DC converter is introduced in [32].

Based on the current research situation of the above-mentioned problem as well as the future outlook, this study proposes the bidirectional converter topology composing of a bidirectional buck-boost and a flyback isolated switched-capacitor through a bidirectional converter. The proposed converter is shown in Figure 1. The main contribution of this proposed converter is, for the step-up model, the output voltages of the coupled inductor boost and the switched capacitor are connected in series to obtain a high gain with a high conversion efficiency even under hard-switching mode and even a high conversion efficiency even under hard-switching mode. Thanks to these features, the operating range of the proposed can be broadened. In addition, this proposed converter performs energy storage tasks on the battery and is flexible in the bidirectional process of supplying and receiving battery power and DC-link/load.

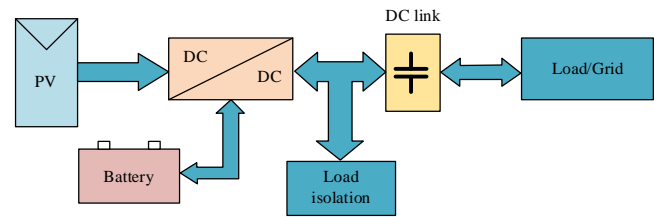


Fig. 1. The proposed DC-DC converter.

This paper consists of four sections with content for each one as Section 1 presents the urgency, settlement, and unresolved for the DC-DC converter problem. The proposed DC-DC-AC converter presents Section 2. The experimental procedures and settings are presented in Section 3. Finally, the conclusion and future research aspects are presented in Section 4.

2. TOPOLOGY AND OPERATING SCENARIOS OF THE PROPOSED CONVERTER

2.1. Topology of the proposed converter

The proposed converter topology is shown in Figure 2, which consists of a low-voltage-side (LVS) circuit and a high-voltage-side (HVS) circuit connected via a high-frequency transformer with a primary winding L_1 two secondary windings L_{21} and L_{22} . The LVS consists of two ports (connected to the PV panel and the battery), a switch M_1 , two capacitors C_1 and C_{bat} , the primary and secondary winding of the transformer L_1 and L_{21} , respectively, and two diodes D_1 and D_2 . The HVS consists of one port (connected to the DC-link/load), the secondary winding of the transformer L_{22} , two switches M_2 and M_3 , two capacitors C_2 and C_3 , a diode D_3 , and an inductor L_1 .

The presented structure utilizes a unidirectional power switch (M_1) and two bidirectional power switches (M_2 and M_3). Among the switches, M_1 is called the main switch because it controls the amount of power generated from the PV system. To simplify the analysis, all capacitances in the circuit are assumed that they have sufficient value such that the voltages across them are constant within a switching period. The pulse width modulation (PWM) strategy for each scenario is explained for the control of the duty cycle that results in the switching of the power switches. The recovery stage is constituted by the diode D_1 and the capacitor C_1 (explanation of the working modes in this recovery stage is fully described in [33]) for four scenarios will be explained in Section 2.1.

The switching states are created based on the binary variable to express the converter operation through a combination of three switches. In this study, the switching states are shown in Table 1 which has eight switching states, and to simplify the analysis, the proposed converter is analyzed by four operation scenarios corresponding to four switching states 5, 7, 3, and 2, namely ST-1, ST-2, ST-3, and ST-4, respectively.

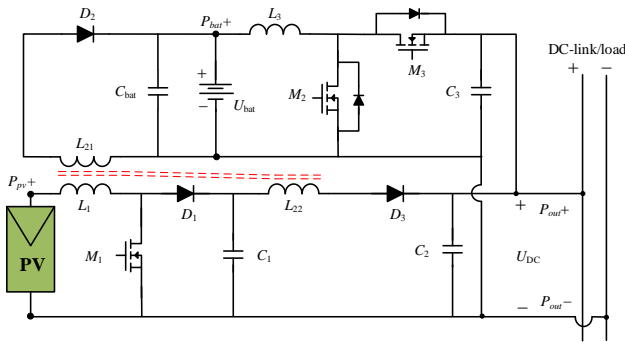


Fig. 2. The proposed bidirectional DC-DC converter.

Table 1. The proposed switching states

State	M ₁	M ₂	M ₃	Topology state
1	0	0	0	Not operation
2	0	0	1	TS-4
3	0	1	0	TS-3
4	0	1	1	-
5	1	0	0	TS-1
6	1	0	1	-
7	1	1	0	TS-2
8	1	1	1	-

2.2. Operating scenarios

Case 1: Topology state 1 (TS-1), in this case, the resonant circuit structure is the one-input and two-output type as shown in Figure 3. This case will operate when the PV is exposed to sunlight and becomes the source of energy for charging energy to the battery, supplying energy to the DC-link/load, and dissipating energy on the inductors L₁, L₂₁, and L₂₂. This state usually occurs during the daytime when there is maximum sunlight. The gate signal with the duty ratio (d₁) is applied to M₁, meanwhile, M₂ and M₃ are turned OFF. When the converter operates in the steady-state, four operating modes depending on the switching period of M₁, and the steady-state waveforms of the four modes are depicted in Figure 4.

Mode 1: t ∈ [t₀, t₁]: M₁ is ON, the coupled inductors work in the flyback and boost states to store the energy from the PV. The output of rectifier diodes D₂ and D₃ are reverse-biased leading to the currents through i_{D₂} and i_{D₃} are zero. Due to the leakage inductor of L₁, the secondary-side currents through inductors i_{L₂₁} and i_{L₂₂} are zero. The stored energy in capacitor C₂ transfers to the DC-link/load and the stored energy in capacitor C_{bat} transfers to the battery.

Mode 2: t ∈ [t₁, t₂]: M₁ is OFF at t₁, while the diodes D₁, D₂, and D₃ are forward-biased. The energies of the leakage

inductor L₁ and the magnetizing inductor L₁ are released to charge the clamp capacitor C₁ via the rectifier diode D₁. Therefore, the energy of L₂₁ and L₂₂, and PV transfer to the output DC-link/load and battery. At t₂, the capacitor C₁ charges full, the leakage inductor of L₁ current decreases due to the existence of L₂₂.

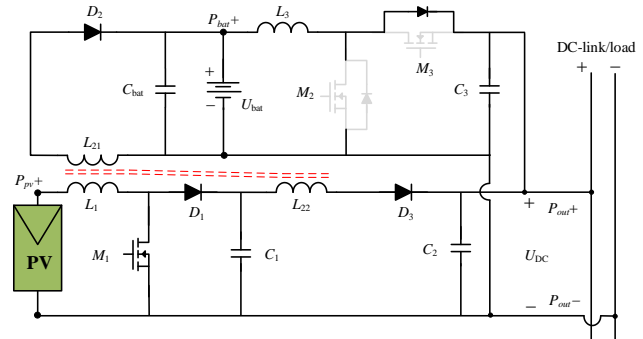


Fig. 3. The topology of the proposed converter under operating TS-1.

Mode 3: t ∈ [t₁, t₂]: At t₂, M₁ is OFF, the diodes D₁, D₂, and D₃ is ON. The energy of secondary-side windings of the coupled inductor L₂₁ and L₂₂, and capacitor C₁ transfer to the DC-link/load and battery. At t₃, the energy leakage inductor L₁ is zero, the energy of the capacitor C₁ discharges to the L₂₂.

Mode 4: t ∈ [t₂, t₃]: Continuing Mode 3, the energy of the DC-link/load and the battery is taken from capacitors C₁ and secondary-side windings L₂₁, L₂₂. The increased output voltage is due to the energy stored windings in the transformer. Along with the self-inductance, mutual between the windings and frequency of main power switch M₁.

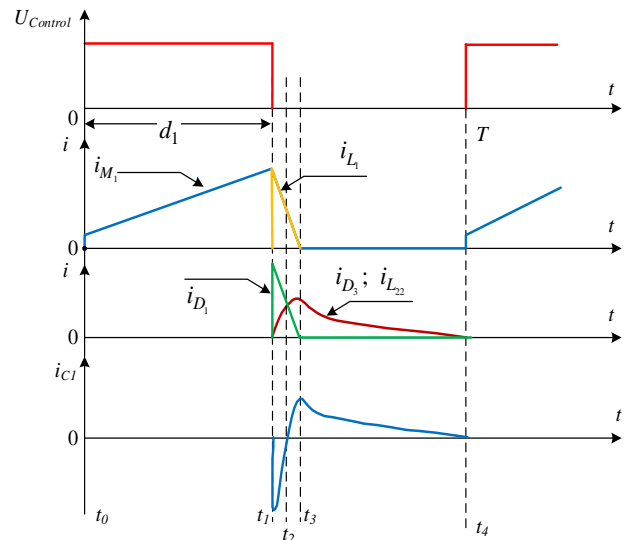


Fig. 4. The waveforms of the proposed converter under operating TS-1.

The converter operates as a flyback and a coupling magnetic boost converter. The flyback converter is the turn ratio of $N_1 = n_{21} / n_1 > 1$ (where n_1 and n_{21} are the numbers of turns of L_1 and L_{21} , respectively), with the aim is to increase the voltage (the reason is that the battery voltage is greater than the PV voltage). This leads to the increased efficiency during the charge for the battery, and it can satisfy the following condition

$$U_{DC-link} > U_{Bat} > U_{PV} \quad (1)$$

Furthermore, the connection of the proposed converter for the systems with isolated loads is not affected the whole system. The boost converter is the turn ratio $N_2 = n_{22} / n_1 > 1$ (where n_{21} is the number of turns L_{22}). Besides, when the battery is fully charged, the energy from PV continues transferring to the DC-link by coupling a magnetic boost.

The proposed converter, operating in this case, can be made from the isolation circuit that has just been charged to the battery.

Case 2: Topology state 2 (TS-2), in this case, the resonant circuit structure is the two-input and two or one-output type as shown in Figure 5. The power is transferred from PV to the DC-link/load. The battery is charged fully, the D_2 is OFF. The D_2 has not only one task for rectifying current and voltage from the output of L_{21} but also cutting out the battery when the battery is fully charged. It does not matter to the control mode in the converter. Therefore, this is also a solution to simplify the control circuit. In this converter structure, the isolated circuit uses the charging characteristics of the battery to calculate the L_{21} winding value, and the voltage on the top of the two windings is considered constant. The amount of energy is charged to the battery depends on the increased voltage [34-35]. So, it is required to discharge the energy from the battery to supply the needed. The converter is directly derived from common and well-known boost as operation boost basic and boost coupling magnetic topologies as TS-1. In applications where high voltage gain is required, boost coupling magnetic topologies typically imply improved performance and efficiency [33,36-37].

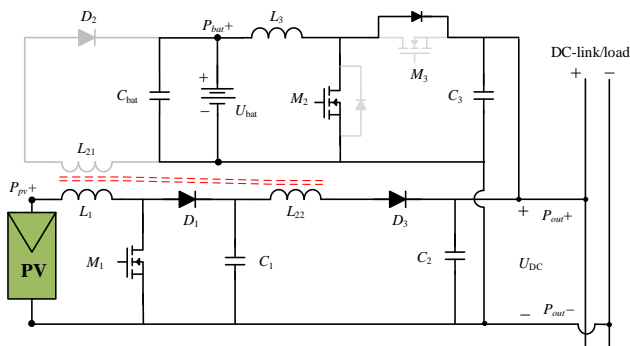


Fig. 5. The topology of the proposed converter under operating TS -2.

In this case, the switches M_1 and M_2 are operating, M_3 is

not operating. Two gate signals with the duty ratios of d_1 , d_2 are applied to M_1 and M_2 . At the time that the battery is fully charged and the diode D_2 is reverse-biased. So, the inductor L_{21} is an open circuit in this state. When the battery discharges, its voltage is less than the voltage on $L_{21:2}$ [34-35], and next period, the state will change the same as Case TS-1 as shown in Figure 6.

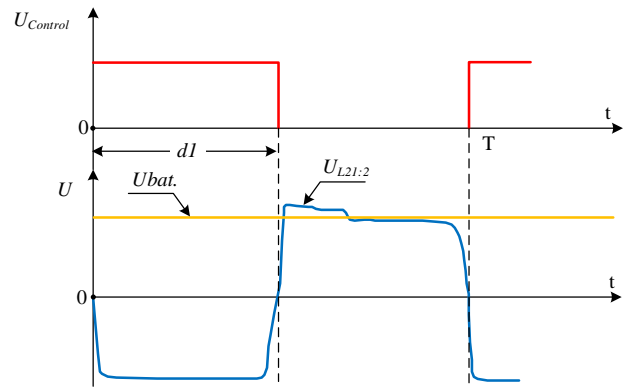


Fig. 6. The waveforms of the voltage under operating TS -2.

Case 3: Topology states 3 and 4 (TS-3 and -4), in this case, the circuit diagram of the proposed bidirectional converter (BC) is given in Figure 7.

For state ST-3, the energy of the battery transfer to the DC-link/load as shown in Figure 7, the converter works as a boost converter, in which the M_2 is operating, the M_1 and M_3 are not operating. The source of energy is provided by the battery that has been maintained at a specific voltage during two operation states. This case is operating during low light conditions when the power generated from the PV panel is very low. In this case, L_3 will be charged/discharged. For state TS-4, the energy in the battery is reduced to the smallest value, the PV does not provide the energy and at the time, the energy in the DC line is connected to other renewable energy sources that are redundant, leading to the battery being charged the energy from the DC line.

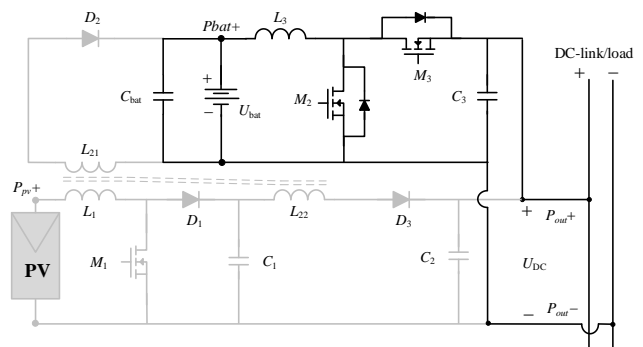


Fig. 7. The topology of the proposed converter under operating TS -3 and 4.

2.3. Control strategy

As described above, the proposed converter can operate in four states. It is necessary to set input generation sources and output voltage through various control variables to control the converter in each state. The primary aim of this paper is to maximize the energy from the PV [38-40] and minimize the energy drawn from the grid. In this case, the status that the battery is rechargeable from DC-link is called a load on the DC-link. The control scheme makes sure that the battery voltage is maintained along with a constant DC-link voltage. Since the source of energy is not constant, several modes can be made in terms of the control aspect to achieve the two goals mentioned above.

This proposed converter is very convenient in the control design combined with feedback loops. So that the power level can be optimized by alternating as discussed in the previous section. Therefore, equations in each mode in each scenario are also considered simply. The states of the four switches M_1 , M_2 , and M_3 are controlled with the help of a proportional plus integral controller. The PWM strategy is such that it prohibits the situation of the forbidden states as listed in Table 1. The switching frequency is set at 12,5 kHz. The gate signals for switches are directly controlled by the microcontroller. This is done to avoid the situation of the forbidden states. As a result, the mode pulse width of the four switches is never the same, hence avoiding a short circuit.

For Case TS-1, the voltage of the DC-link/load and the battery can be determined as follows, respectively

$$U_{DC-link} = U_{C2} = U_{C3} = U_{PV} \frac{d_1}{1-d_1} (1 + N_2)((1+k) / 2) \tag{2}$$

$$U_{Bat} = U_{Cbat} = U_{PV} \frac{d_1}{1-d_1} N_1 \tag{3}$$

where N_1 is the turns ratio between n_1 and n_{21} , N_2 is the turns ratio from n_1/n_{22} , and k is the coupling factor of the transformer in the converter.

From condition in Eq. (1), we have

$$N_2 > N_1 \tag{4}$$

The PWM strategy is simple for the switch M_1 by the loop controlled for the capacitor C_2 .

For Case TS-2, the DC gain for output DC-link/load isolation can be calculated according to the equation:

$$\frac{U_{DC-link}}{U_{Bat}} = d_2 \tag{5}$$

Comparing the Eqs. (1), (3), and (5) it has two following conditions:

Firstly, if output DC-link/ load is equal:

$$d_2 > d_1 \tag{6}$$

Secondly, if output DC-link/load is different than output isolation less:

$$d_2 \geq d_1 \tag{7}$$

The PWM strategy is the same as the one employed in Case TS-1. Here also, the three switches have to avoid a short circuit condition thereby all three switches cannot be ON at any given point in time. The control aspect varies slightly from TS-1. There is additional control for the M_2 . In this state, there are three closed-loop controls. As shown in Figure 8, the first loop shows the control strategy for the voltage of capacitor C_2 , the output of this controller is given to the gate signal of switch M_1 . The second loop is the control for the voltage of capacitor C_3 , the output of this controller is given to gate M_2 . With additional control of mode for M_2 operation or non-operation is the third effectuated loop.

For Case TS-3, the DC gain for DC-link/load output is determined Eqs. 5, 6, and 7. The loop is controlled the same as the second loop in TS-2.

For case TS-4, the DC gain for battery output:

$$\frac{U_{Bat}}{U_{DC-link}} = d_3 \tag{8}$$

The PWM strategy in this state is simple by the fourth loop controlled for the switch M_3 depending on the value of the battery and the value of the parameters in the converter. Thus, the controller simply uses four loops in the four operating states of the converter.

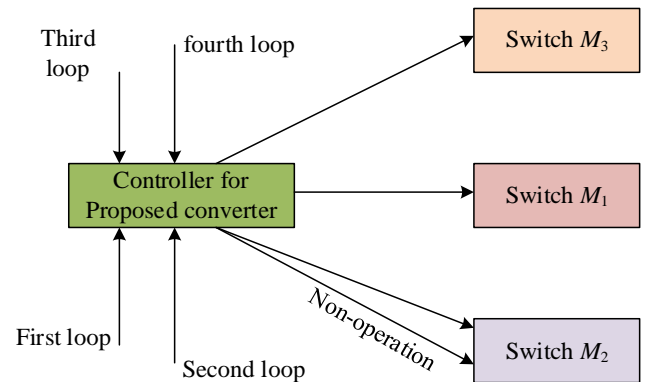


Fig. 8. Diagram of the proposed control scheme.

3. SIMULATION AND EXPERIMENTAL RESULTS

This part gives the simulated and experimental results for each case. The circuit diagram for each state along with the detailed analysis. A comparison between the experimental and simulation results is realized to validate the configuration.

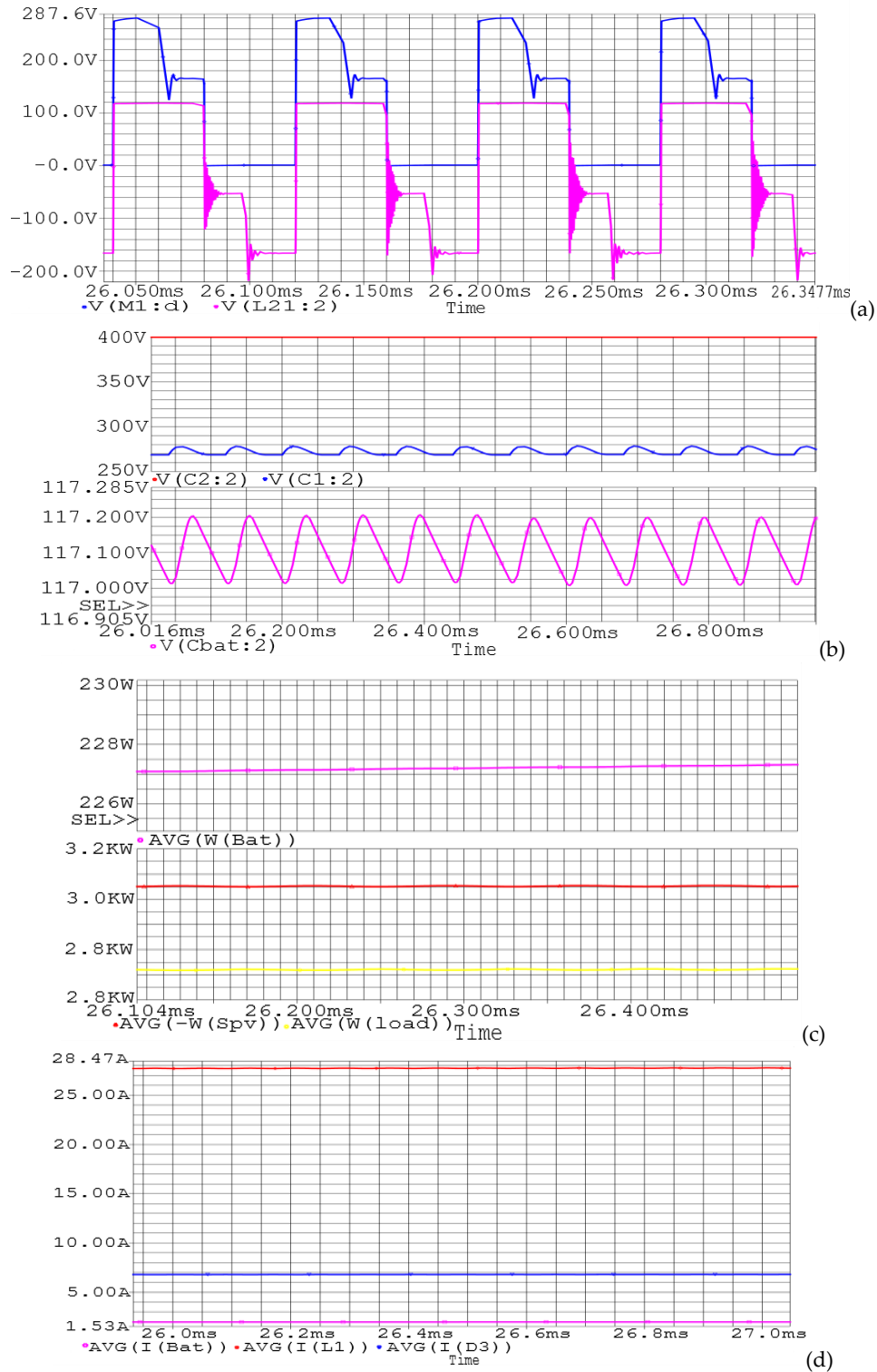


Fig. 9. Simulation results of the operation in Case TS-1: (a) Voltage of switch M_1 and D_2 , (b) Voltage of the capacitors and DC-link/load, (c) Power of the PV, Battery, and DC-link/load, (d) Currents by PV panels, DC-link/load, and battery.

3.1. Simulation Results

The simulations are performed in Orcad software to evaluate the operation of the proposed converter. The parameter values for simulation are the turn ratios of the

transformer are $N_1 = 2,2$ and $N_2 = 10$; the coupling factor is $k = 0,95$; the capacitors are $C_1 = 47 \mu\text{F}$, $C_{bat} = 470 \mu\text{F}$, $C_2 = C_3 = 1000 \mu\text{F}$; the consumed power of PV and Battery is (500-3500) W; the voltage of DC-link/load is 400V, the voltage of the battery is 120V. The used power switches

are MOSFET for M_1 to be IRF7799L2, M_2 to be IRFB4137, and M_3 to be IRFH5025.

Figure 9 shows the simulation results of the operation converter in *Case TS-1*. Figure 9 (a) shows the voltage stress waveform of switch M_1 , diode D_2 . The power switches are chosen according to the voltage stress of the main switches with considering the maximum voltage of the PV from 70 V to 110 V. The voltages, crossing the capacitors C_1 , C_2 , and C_{bat} , are illustrated in Figure 9 (b), in which the value of capacitor C_1 is changed according to the voltage value of the PV. The output voltage of capacitor C_2 is 400V. Figure 9 (c) shows the generated power of the source, load, and battery. The current of the battery, L_1 , and D_3 provided by the PV source is shown in Figure 9 (d).

Observing this figure shows that the battery is operating at the charging state and the current, flowing from the DC-link/load, has low.

Figure 10 shows the simulated results for *Case TS-2*, in which the voltage stress waveform is proportional to switches M_1 and M_2 , the M_2 is controlled by duty ratio condition in Eq. (6) as shown in Figure 10 (a). So, the power switch M_2 is chosen according to voltage stress but other with the selection of the power switch M_1 . The average input current of PV and Battery and output current of DC-link of the converter are shown in Figure 10 (b). Figure 10 (c) shows the generated powers of PV, load, and battery sources.

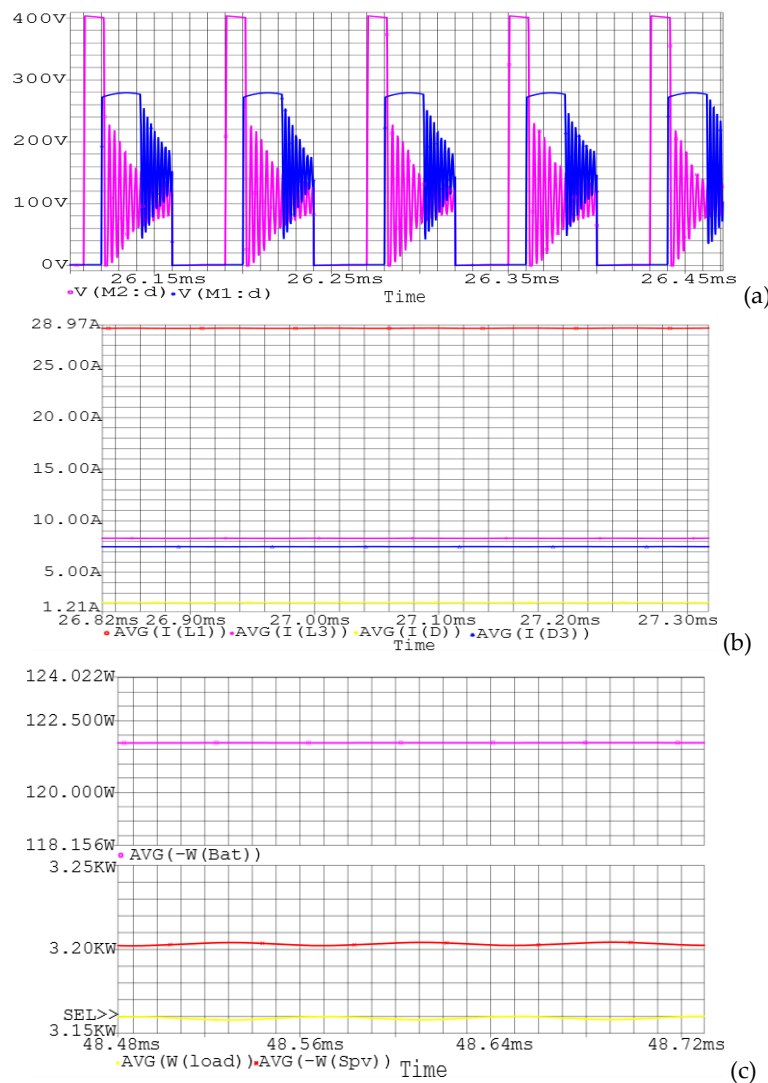


Fig. 10. Simulation results of the operation in *Case TS-2*: (a) Voltage of switch M_1 and M_2 ; (b) currents of the PV panel, battery, and DC-link; (c) Power of the PV, battery, and DC-link.

For *Case TS-3*, the simulated results are like *Case TS-2*. Figure 11 is illustrated the operation of the proposed converter in *Case TS-4*. The voltage stress waveform proportional to switch M_3 is shown in Figure 11 (a) and the

M_3 is controlled by duty ratio condition in Eq. (8). Figure 11 (b) shows the generated powers of the DC-link/load and battery sources.

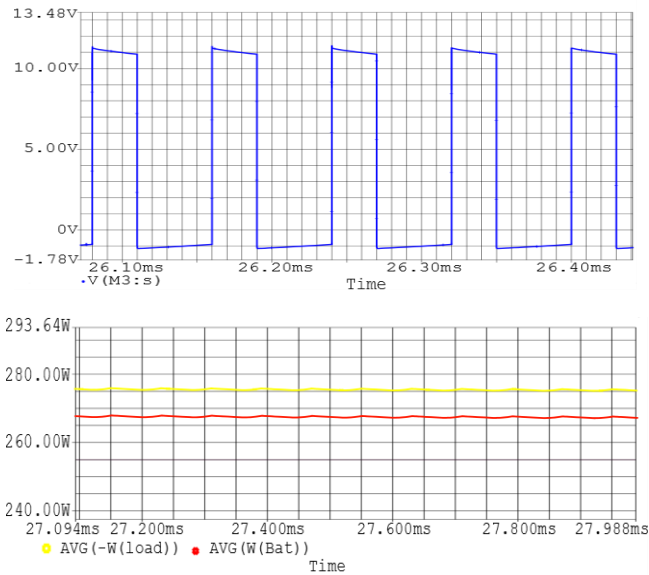


Fig. 11. Simulation results of the operation in Case TS-4: (a) The voltage of switch M_3 , (b) Power DC-link and battery.

3.2. Experimental Results

The laboratory prototype is equipped for the tests as shown in Figure 12. The PV solar panel is connected to the

Battery system via the proposed converter. The converter is connected to the DC-link/load and load isolation as well as the microcontroller. The switches in the converter are connected to the gate drivers. The control circuit for this converter follows the conditions analyzed in Section 2.3. The oscilloscope is used to measure the input and output of the current, voltage, and power under each scenario. The tests are carried out with almost real-world conditions. However, the majority of the laboratory tests were conducted with the use of the resistance load. The converter input (the output of the PV source) is placed in a converted current filter capacitor.

The converter composes of five diodes, four capacitors, and one transformer and it is controlled by three MOSFET switches. This proposed converter can transmit the power smoothly between DC-link/load and the AC grid. The conversion efficiency for the proposed converter is determined by measuring the input and output of the voltage and current under all operation modes for different power levels and PV voltage levels. The experimental results are presented in Figures 13 and 14.



Fig. 12. Hardware picture of the proposed converter.

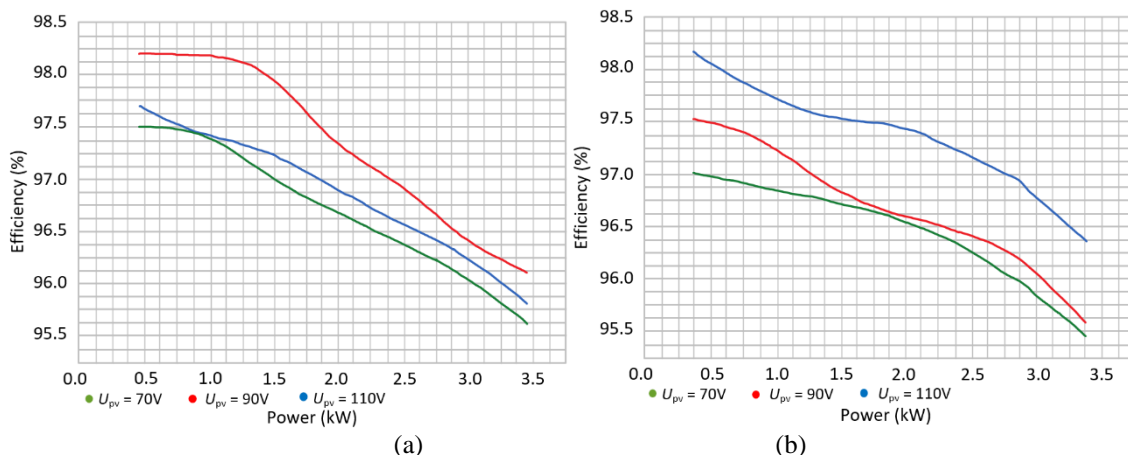


Fig. 13. Efficiency power of the operation converter: (a) PV to DC-link/load and battery operation, (b) PV and battery to DC-link/load.

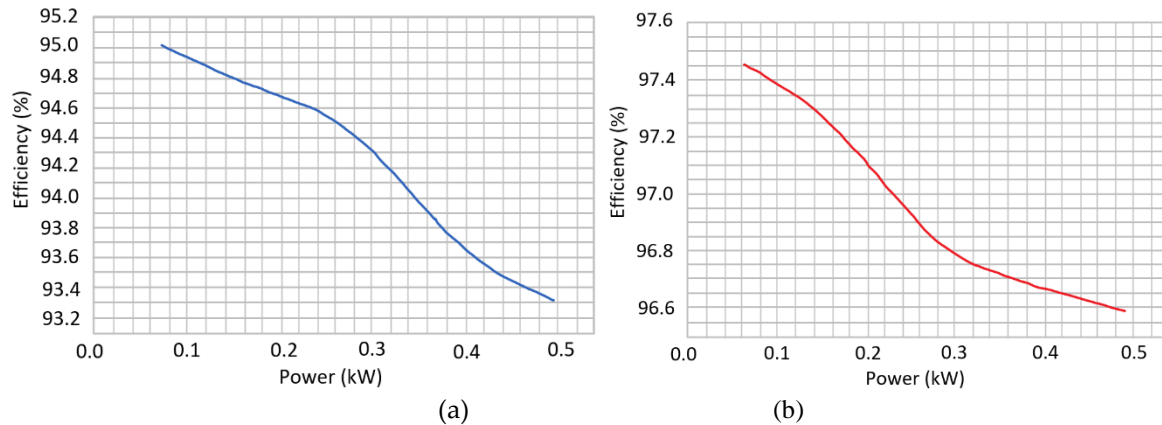


Fig. 14. Efficiency power of the proposed converter: (a) Battery to DC-link/load and (b) DC-link/load to the battery

Observing the obtained result shows that. The highest efficiencies are measured in the PV to battery operation with a maximum efficiency of 97,8% with $U_{pv}=110V$ at 1.2kW and the lowest efficiency of 96% with $U_{pv}=70V$ at 2.5kW.

The lowest efficiencies are measured in the battery to the operating DC-link, as shown in Figure 14 (a). Overall, the worst operation case in terms of efficiency is encountered when the converter is operating at high duty cycles for switch M_2 , such that the MOSFET has experienced high current stress. Figure 14 (b) shown the efficiency of the results for Case TS-4. It can be seen that the maximum efficiency of this converter is 97.5% at 450W and the rated efficiency is approximately 96%.

4. CONCLUSIONS

This study proposed a new partially-isolation, bidirectional DC-DC converter with the least number of switches has been proposed for PV-battery-DC microgrid applications. The proposed topology is suitable for step-up and step-down voltage gains for PV conditioning systems. The topology structure is highly efficient. The simulation and experimental results have shown that the converter is not only capable of maximum power point tracking control (MPPT) for the PV panel when there is solar radiation, more also can control the charge/discharge of the battery by its characteristic.

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